Abstracts

Finite-Difference Time-Domain Analysis of Microwave Circuit Devices on High Performance Vector/Parallel Computers (Short Papers)

S.D. Gedney. "Finite-Difference Time-Domain Analysis of Microwave Circuit Devices on High Performance Vector/Parallel Computers (Short Papers)." 1995 Transactions on Microwave Theory and Techniques 43.10 (Oct. 1995 [T-MTT]): 2510-2514.

In this paper, an efficient finite-difference time-domain algorithm for high performance distributed memory vector/parallel computers is presented. The algorithm is developed in a manner which requires only one interprocessor communication per time step. Illustrated examples based on the analysis of microwave circuit devices are presented demonstrating the efficiency and scalability of the finite-difference time-domain algorithm.

❑ Return to main document.